

IN THE SPECIFICATION

Please amend paragraphs [0002]-[0004] as follows:

[0002] United States Patent Application Serial No. 10/085,331\*\*\*/xxx,xxx, entitled "MULTIPLE NONVOLATILE MEMORIES", by Ronald Cocchi, et. al., Attorney Docket No. PD-200335, filed on the same date herewith February 28, 2002;

[0003] United States Patent Application Serial No. 10/085,346\*\*\*/xxx,xxx, entitled "HIDDEN IDENTIFICATION", by Ronald Cocchi, et. al., Attorney Docket No. PD-200336, filed on February 28, 2002 the same date herewith; and

[0004] United States Patent Application Serial No. 10/085,920\*\*\*/xxx,xxx, entitled "DEDICATED NONVOLATILE MEMORY", by Ronald Cocchi, et. al., Attorney Docket No. PD-200337, filed on February 28, 2002 the same date herewith.

Please amend paragraph [0046] as follows:

[0046] FIG. 4A is a diagram of a representative data stream. The first packet segment 402 comprises information from video channel 1 (data coming from, for example, the first video program source 200A). The next packet segment 404 comprises computer data information that was obtained, for example from the computer data source 208. The next packet segment 406 comprises information from video channel 5 (from one of the video program sources 200). The next packet segment 408 comprises program guide information such as the information provided by the program guide subsystem 206. As shown in FIG. 4A, null packets 410 created by the null packet module 212 may be inserted into the data stream as desired. The following packet segments 412-416 comprise, respectively, audio information from channel 1, video information from channel 1 and audio information from channel 2.

Please amend paragraph [0051] as follows:

[0051] The transport module 508 performs many of the data processing functions performed by the IRD 126. The transport module 508 processes data received from the FEC decoder module 506 and provides the processed data to the video MPEG decoder 514 and the audio MPEG decoder 517. As needed the transport module employs system RAM 528 to process the data. In one embodiment of the present invention, the transport module, video MPEG decoder and audio MPEG decoder are all implemented on integrated circuits. This design promotes both space and

power efficiency, and increases the security of the functions performed within the transport module 508. The transport module 508 also provides a passage for communications between the microcontroller 510 and the video and audio MPEG decoders 514, 517. As set forth more fully hereinafter, the transport module also works with the conditional access module (CAM) 512 to determine whether the subscriber receiving station 110 is permitted to access certain program material. Data from the transport module can also be supplied to external communication module 526.

Please amend paragraph [0054] as follows:

[0054] Audio data is likewise decoded by the MPEG audio decoder 517 (e.g., using the audio RAM 520). The decoded audio data may then be sent to a digital to analog (D/A) converter 518. In one embodiment of the present invention, the D/A converter 518 is a dual D/A converter, one for the right and left channels. If desired, additional channels can be added for use in surround sound processing or secondary audio programs (SAPs). In one embodiment of the invention, the dual D/A converter 518 itself separates the left and right channel information, as well as any additional channel information. Other audio formats may similarly be supported. For example, other audio formats such as multi-channel DOLBY DIGITAL AC-3 may be supported.